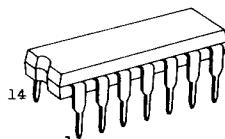


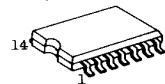
TC4073BP/TC4073BF TRIPLE 3 INPUT AND GATE
TC4081BP/TC4081BF QUAD 2 INPUT AND GATE
TC4082P DUAL 4 INPUT AND GATE

TC4081BP/BF, TC4073BP/BF and TC4082BP are positive logic AND gates with two inputs, three inputs and four inputs respectively.

Since all the outputs of these gates are equipped with the buffer circuits of inverters, the input/output propagation characteristic has been improved and variation of propagation time caused by increase of load capacity is kept minimum.



DI P14 (3D14A-P)



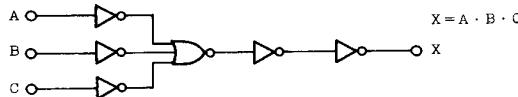
MFP14 (F14GB-P)

ABSOLUTE MAXIMUM RATINGS

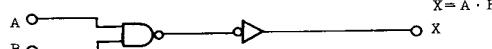
CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	V _{SS} -0.5 ~ V _{SS} +20	V
Input Voltage	V _{IN}	V _{SS} -0.5 ~ V _{DD} +0.5	V
Output Voltage	V _{OUT}	V _{SS} -0.5 ~ V _{DD} +0.5	V
DC Input Current	I _{IN}	±10	mA
Power Dissipation	P _D	300(DIP)/180(MFP)	mW
Operating Temperature Range	T _A	-40 ~ 85	°C
Storage Temperature Range	T _{stg}	-65 ~ 150	°C
Laded Temp./Time	T _{sol}	260°C • 10 sec	

LOGIC DIAGRAM

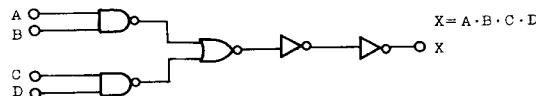
1/3 TC4073BP/BF



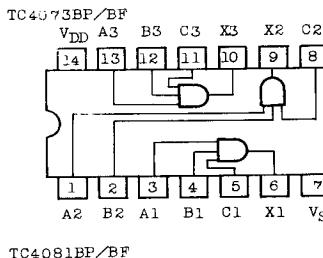
1/4 TC4081BP/BF



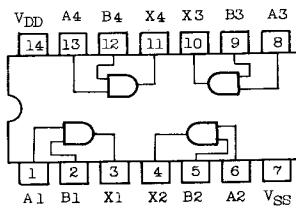
1/2 TC4082BP



PIN ASSIGNMENT (TOP VIEW)



TC4081BP/BF



TC4082BP

