TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4024BP,TC4024BF,TC4024BFN

TC4024B 7 Stage Ripple-Carry Binary Counter/Dividers

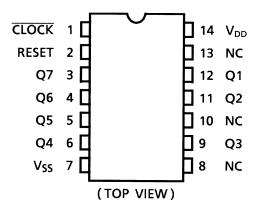
TC4024B is 7 stage ripple carry type binary counter having asynchronous clear function.

 $\begin{tabular}{ll} \hline The counter advances its counting state by falling edge of \\ \hline \hline CLOCK & input. \\ \hline \end{tabular}$

When RESET input is placed at "H", all the internal flip-flop are reset making all the outputs Q1 through Q7 to be "L" regardless of $\overline{\text{CLOCK}}$ input.

This is suitable for frequency divider circuits and control circuits.

Pin Assignment



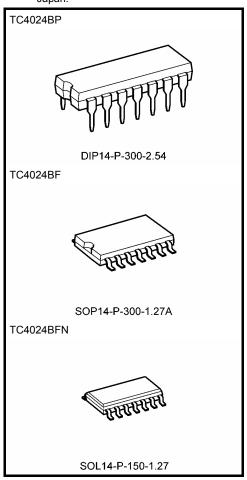
Truth Table

$\overline{CLOCK}\ \Delta$	RESET	Output State		
* H All Outputs = "L"				
	L	No Change		
\neg	L	Advance to Next State		

Δ: Level change

*: Don't care

Note: xxxFN (JEDEC SOP) is not available in Japan.

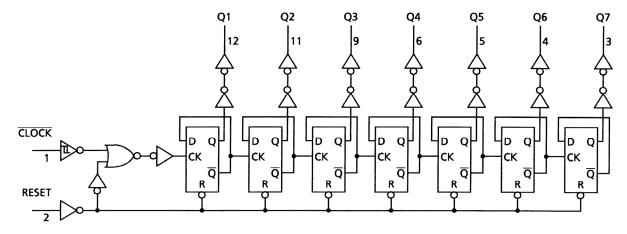


Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.)



Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output voltage	V _{OUT}	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges $(V_{SS} = 0 V)$ (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V_{DD}	V

2

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .



Static Electrical Characteristics ($V_{SS} = 0 V$)

01		Sym-	Test Condition		-40°C		25°C			85°C		
Charac	teristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage			 I _{OUT} < 1 μA	5	4.95	_	4.95	5.00	_	4.95	_	
		V _{OH}	$V_{IN} = V_{SS}, V_{DD}$	10	9.95	_	9.95	10.00	_	9.95	_	V
J			VIN - VSS, VDD	15	14.95		14.95	15.00	_	14.95	_	
			 I _{OUT} < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V _{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V
J			VIN - VSS, VDD	15		0.05	—	0.00	0.05	_	0.05	
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	h current	IoH	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		l _{OL}	V _{OL} = 0.4 V	5	0.61	_	0.51	1.2	_	0.42	_	mA
Output low	, ourront		$V_{OL} = 0.5 V$	10	1.50	_	1.30	3.2	_	1.10	_	
Output low	Current		V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		VIH	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V
Innut biab	voltogo		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
Input high	voitage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			I _{OUT} < 1 μA									
			V _{OUT} = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low voltage		V _{IL}	V _{OUT} = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
			V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			I _{OUT} < 1 μA									
Input	"H" level	l _{IH}	V _{IH} = 18 V	18	_	0.1	_	10^{-5}	0.1	_	1.0	^
current	"L" level	IJL	V _{IL} = 0 V	18		-0.1	_	-10^{-5}	-0.1	_	-1.0	μА
			$V_{IN} = V_{SS}, V_{DD}$	5	_	5	_	0.005	5	_	150	
	Quiescent supply current			10	_	10	_	0.010	10	_	300	μА
303110			(Note)	15		20		0.015	20	_	600	

3

Note: All valid input combinations.



Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

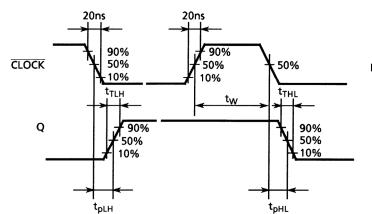
Observatoristics	O. was boat	Test Condition	N 41:	т	Mary	l lait	
Characteristics	Symbol		V _{DD} (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
(low to high)	t _{TLH}	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
(high to low)	t _{THL}	_	10	_	35	100	ns
(flight to low)			15	_	30	80	
Propagation delay time			5	_	140	360	
(CLOCK -Q1)	t _{pLH}	_	10	_	70	160	ns
(CLOCK -QT)			15	_	50	130	
Dran a pation dalou time			5	_	140	360	
Propagation delay time (CLOCK -Q1)	t _{pHL}	_	10	_	70	160	ns
(CLOCK -QT)			15	_	50	130	
Decree and the state of the sta			5	_	400	1200	
Propagation delay time	t _{pLH}	_	10	_	160	520	ns
(CLOCK -Q7)	·		15	_	115	430	
Decree and the state of the sta			5	_	400	1200	
Propagation delay time	t _{pHL}	_	10	_	160	520	ns
(CLOCK -Q7)			15	_	115	430	
D "			5	_	140	280	
Propagation delay time	t _{pHL}	_	10	_	70	120	ns
(RESET-Q)	F		15	_	50	100	
			5	3.5	14	_	
Max clock frequency	f _{CL}	_	10	8.0	30	_	MHz
			15	12.0	40	_	
			5		I	I	
Max clock input rise time	trCL	_	10	No limit			μS
Max clock input fall time	t _{fCL}		15				
			5	_	40	140	
Max clock pulse width	t _W	_	10	_	20	60	ns
			15	_	15	40	
			5	_	40	200	
Max pulse width	t _{WH}	_	10	_	20	80	ns
(RESET)			15	_	15	60	
			5	_	0	350	
Minimum removal time	t _{rem}	_	10	_	0	150	ns
			15	_	0	100	
Input capacitance	C _{IN}	_	I	_	5	7.5	pF

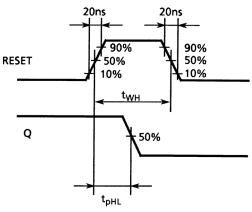
4

Waveforms for Measurement of Dynamic Characteristics

Waveform 1

Waveform 2

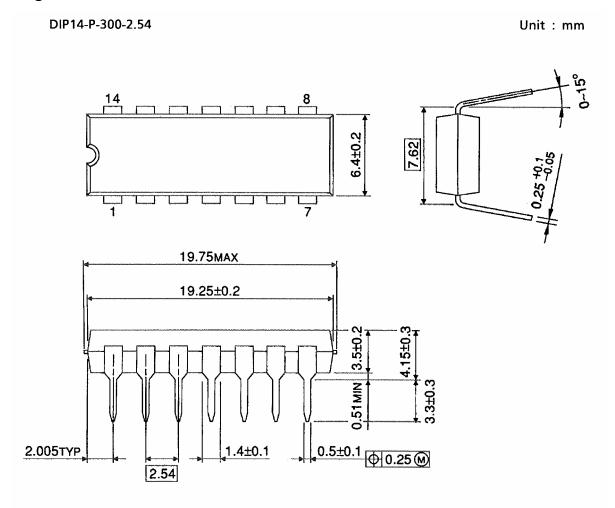




5



Package Dimensions

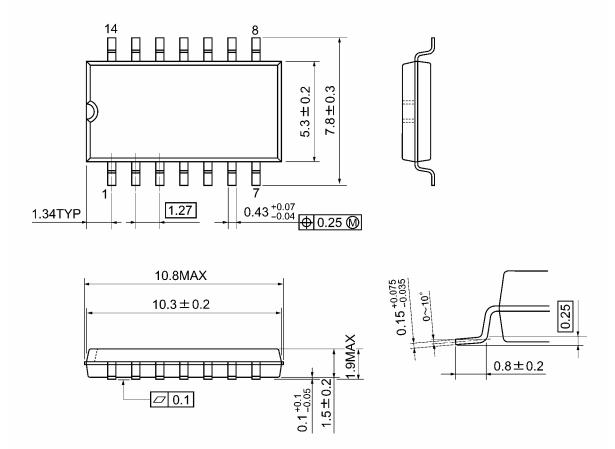


6

Weight: 0.96 g (typ.)

Package Dimensions

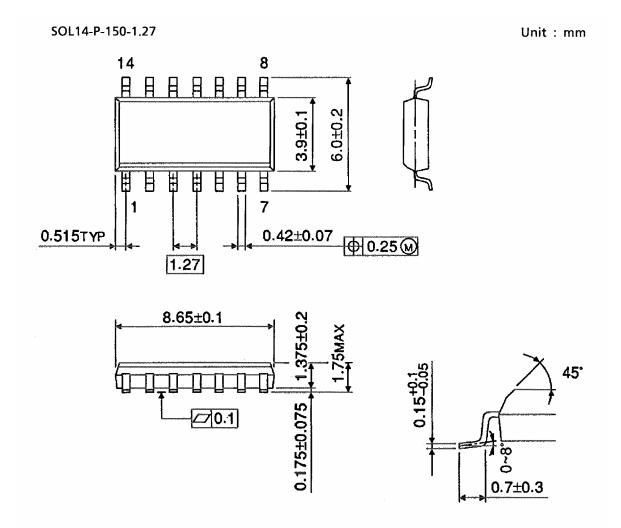
SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



Package Dimensions (Note)



8

Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in his document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which
 manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS
 compatibility. Please use these products in this document in compliance with all applicable laws and regulations
 that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses
 occurring as a result of noncompliance with applicable laws and regulations.

9