TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8162SN

Dual Preamplifier

The TA8162SN is dual preamplifier designed for car stereo tape deck.

This IC contains dual preamplifier and metal/normal tape equalizer control switches.

Features

- High open loop voltage gain
 - $: G_{VO} = 98dB \text{ (typ.) } (V_{CC} = 9 \text{ V, f} = 1 \text{ kHz})$
- Low distortion
 - : THD = 0.035% (typ.)

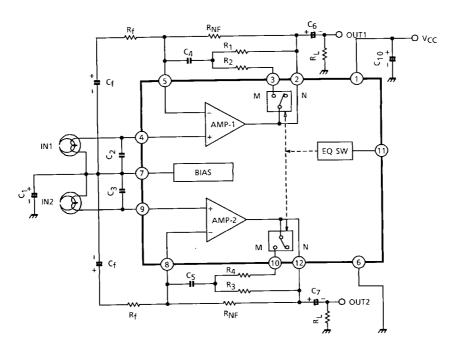
(GV = 40dB, f = 1 kHz, VOUT = 0.5 Vrms)

- Low noise (equivalent input noise voltage)
 - : VNI = 0.9 $\mu Vrms$ (typ.) (Rg = 620 $\Omega,$ BW = 20 Hz~20 kHz, NAB EQ)
- No input coupling capacitor
- Small package: Shrink pitch (1.78 mm) single in-line 12 pin
- Operating supply voltage range: $V_{CC (opr.)} = 6 \sim 16 \text{ V}$

SSIP12-P-1.78

Weight: 0.65 g (typ.)

Block Diagram



Application Information

1. Equalizer control switch

Pin11 is coupled to the base of Q_1 (PNP-Tr) as shown in Figure 1.

The emitter potential of Q_1 is 3.9 Vdc.

Threshold voltage (pin11)

Metal	3.2~V _{CC}
Normal	0~2.4 V

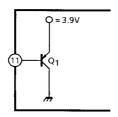


Figure 1

2. C_{2~3}

Capacitor C2/C3 may be required for preventing a instability caused by the pattern layout or interference of external high frequency signal.

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	16	V
Power dissipation	P _D (Note)	750	mW
Operating temperature	T _{opr}	-30~75	°C
Storage temperature	T _{stg}	-55~150	°C

Note: Derated above Ta = 25°C in the proportion of 6 mW/°C.

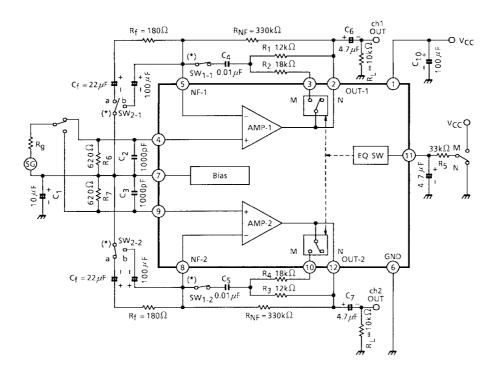
Typ. DC Voltage of Each Terminal (V_{CC} = 9 V, Ta = 25°C, Dual mode test circuit)

Terminal No.	1	2	3	4	5	6	7	8	9	10	11	12
DC-voltage (V)	V_{CC}	3.0	3.0	3.0	3.0	GND	3.0	3.0	3.0	3.0	3.5	3.0

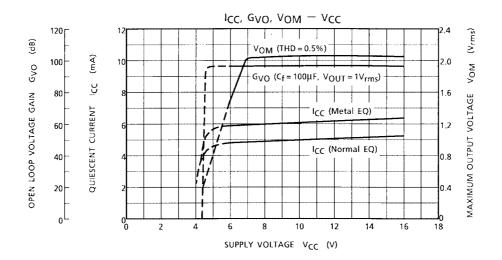
Electrical Characteristics (unless otherwise specified, V_{CC} = 9 V, f = 1 kHz, R_L = 10 k Ω , R_g = 600 Ω , Ta = 25°C, normal EQ)

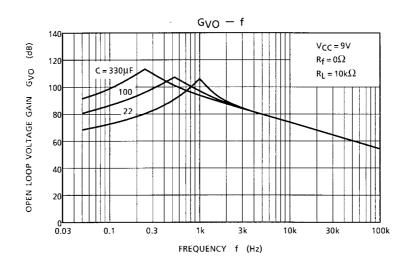
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Quiescent current	ICCQ (1)	_	V _{IN} = 0, Normal EQ	_	5.0	_	mA
	ICCQ (2)	_	V _{IN} = 0, Metal EQ	1	6.0	9.0	IIIA
Open loop voltage gain	G _{VO}	_	$C_f = 100 \mu F, R_f = 0$	_	98	_	dB
Maximum output voltage	V _{OM}	_	THD = 0.5%	1.5	2.0	_	Vrms
Total harmonic distortion	THD	_	V _{OUT} = 0.5 Vrms	_	0.035	0.12	%
Equivalent input noise voltage	V _{NI}	_	R_g = 620 Ω, NAB BW = 20 Hz~20 kHz	_	0.9	1.7	μVrms
Input resistance	R _{IN}	_	_	_	500	_	kΩ
Ripple rejection ratio	R.R.	_	f _{ripple} = 100 Hz, V _{IN} = 1 Vrms	-	55	_	dB

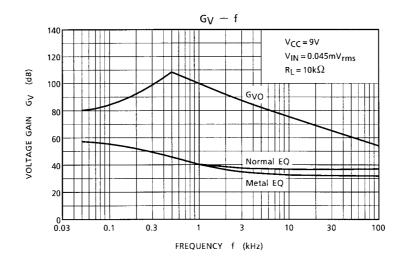
Test Circuit



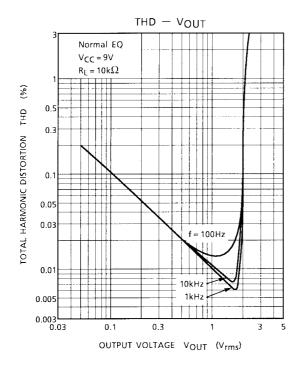
*: G_{VO} Test: SW_{1-1, 2} = OFF, SW_{2-1, 2} = b

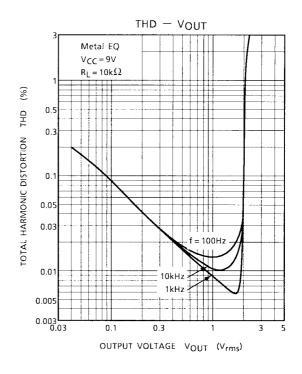


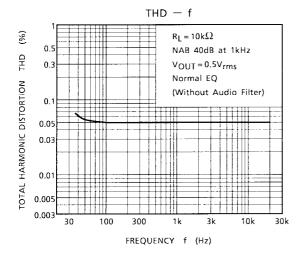


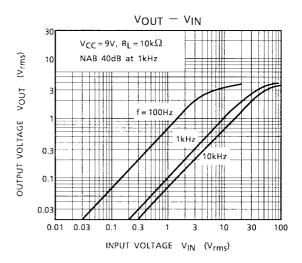


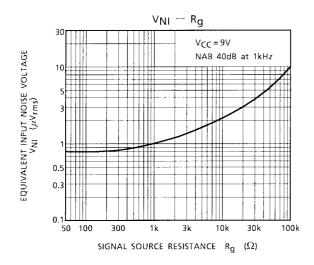
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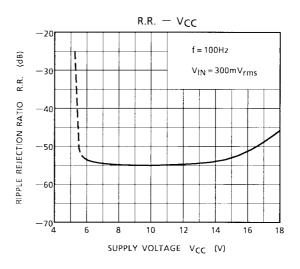


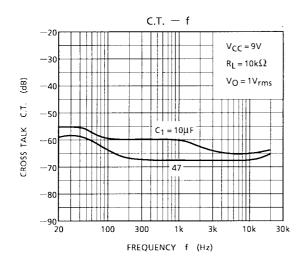


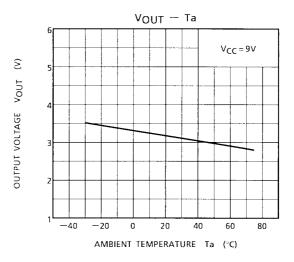


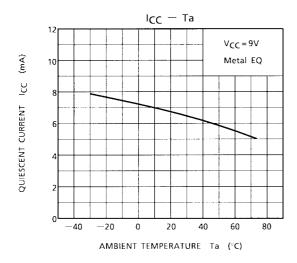






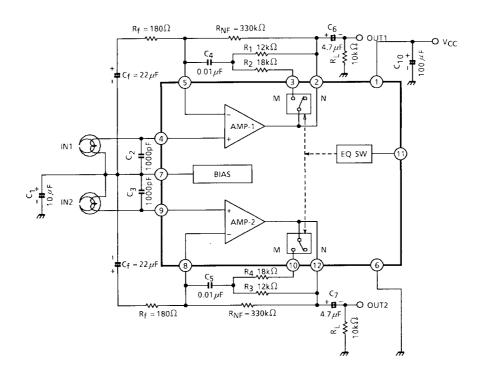






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Application Circuit

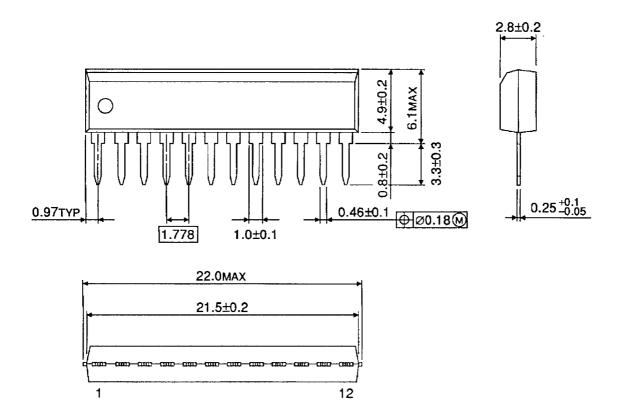


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Package Dimensions

SSIP12-P-1.78 Unit: mm



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Weight: 0.65 g (typ.)

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