TOSHIBA

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA7354P

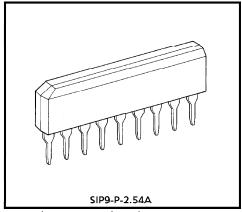
BRIDGE DRIVER

The TA7354P is a Bridge Driver for blashed DC Motor Rotation control.

Forward Rotation, Reverse Rotation, Stop and Braking operations are available.

It's designed for Loading and Reel Motor driver for VTR and Tape Deck, and any other consumer and industrial applications.

TA7354P have Operation Supply Voltage terminal and Motor Driving Supply Voltage terminal independently, therefore Servo control operation is applicable.

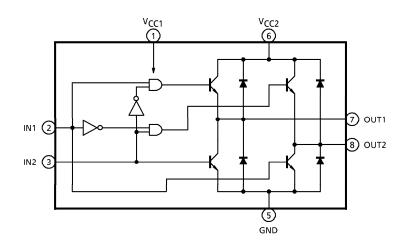


Weight: 0.92g (Typ.)

FEATURES

- Output Current Up to 0.2A (AVE.), and 0.6A (PEAK).
- 4 Function Modes (CW, CCW, STOP and Brake) are Controlled by 2 Logic Signals Fed Into 2 Input Terminals.
- Operating Voltage Range : $V_{CC} = 6 \sim 15V$

BLOCK DIAGRAM



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PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION		
1	V _{CC1}	Supply voltage terminal for control part		
2	IN1	input terminal-1		
3	IN2	input terminal-2		
4	_	No. connection		
5	GND	GND terminal		
6	V _{CC2}	Supply voltage terminal for output part		
7	OUT1	Output terminal-1		
8	OUT2	Output terminal-2		
9	_	No. connection		

FUNCTION

IN1	IN2	OUT1	OUT2	MODE		
1	1	L	L	Brake		
0	1	L	Н	CW/CCW		
1	0	Н	L	CCW / CW		
0	0	High Im	pedance	Stop		

MAXIMUM RATINGS (Ta = 25°C)

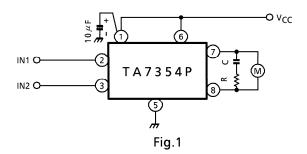
CHARACTERI	STIC	SYMBOL	RATING	UNIT	
Supply Voltage	PEAK	V _{CC} (MAX.)	18	V	
Supply Voltage	OPERATE	V _{CC} (ope.)	15		
Output Current	PEAK	lo (PEAK)	0.6	Α	
Output Current	AVE.	lo (AVE.)	0.2		
Power Dissipation		P _D (*)	0.75	W	
Operating Tempera	ture	T _{opr} - 30~75		°C	
Storage Temperatur	·e	T _{stg}	- 55∼150	°C	

(*) No Heat Sink

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, Ta = 25°C)

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CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Supply Current		l _{CC1}		$V_{CC1} = V_{CC2} = 15V$ $I_O = 0.2A$ Drive mode	_	22	33	mA	
		l _{CC2}		V _{CC1} = V _{CC2} = 15V Brake mode	_	30	38		
		_l cc3		V _{CC1} = V _{CC2} = 15V Stop mode	_	0.2	1		
Saturation	Upper	V _{S1} U	-	V _{CC1} = V _{CC2} = 15V, I _O = 0.1A	_	0.8	1.05	v	
	Lower	V _{S1 L}			_	0.15	0.25		
Voltage	Upper	V _{S2} U	_	V _{CC} = V _{CC2} = 15V, I _O = 0.2A	_	0.9	1.2	V	
	Lower	V _{S2 L}		VCC = VCC2 = 13V, 10 = 0.2A	_	0.3	0.5		
Leakage	Upper	l _L U		V = 15V	_	_	20		
Current	Lower	ILL		V = 13V	_	_	20	μΑ	
Input Voltage 1, 2		V _{IN} (H)		$- \begin{cases} T_j = 25^{\circ}C \\ Pin① \text{ and } pin② \end{cases}$	2.0	_	_	V	
		V _{IN (L)}			_	_	0.8		
Input Current 1, 2		l _{IN1, 2}	_	V _{IN} = 4.5V	_	_	350	μ A	
Diode Forward Voltage		V_{FU}		I _F = 0.2A		1.2	1.6	V	
		V_{FL}		— IF = 0.2A	_	1.0	1.3		

APPLICATION CIRCUIT

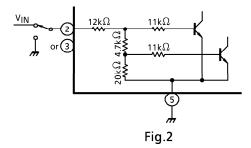


(Note) Fig.1 shows the basic application circuit.

Optimum values of the C, R depend on the inherent constant of a motor and parasitic C, R values around the circuit.

APPLICATION NOTE

(1) Input circuit



Input circuit is shown in Fig.2. It's a "High Active" type.

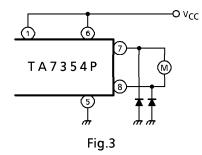
If a voltage above specified V_{IN} (H) value fed into input terminal that means "Logic 1", and the voltage less than V_{IN} (L) or connect to GND means "Logic 0".

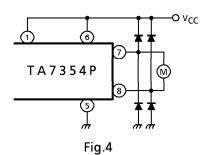
And the circuit have a hysteresis for stable operation. (See Fig.1)

- (2) If the braking operation is so loose, connect a additional diode between each output to GND. (See Fig.3)
- (3) If the back electromotive pulse generated in output coil is so strong.

 Internally connected back electromotive suppression diode may be damaged by this pulse.

 In such a case connect a additional diode between each output to V_{CC}. (See Fig.4)

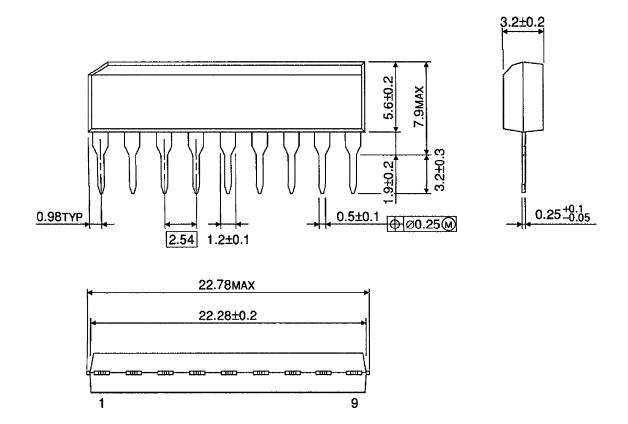




(4) Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING

SIP9-P-2.54A Unit: mm



Weight: 0.92g (Typ.)