MOC3040, MOC3041, MOC3042, MOC3043 MOC3040X, MOC3041X, MOC3042X, MOC3043X



# OPTICALLY COUPLED BILATERAL SWITCH LIGHTACTIVATED ZERO VOLTAGE CROSSING TRIAC



### **APPROVALS**

• UL recognised, File No. E91231 Package Code " GG " or " TT "

### 'X'SPECIFICATIONAPPROVALS

- VDE 0884 in 3 available lead form:-
  - STD
  - G form
  - SMD approved to CECC 00802

### DESCRIPTION

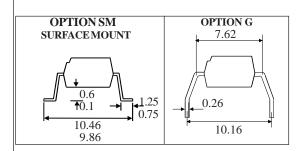
The MOC304\_Series are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode coupled with a monolithic silicon detector performing the functions of a zero crossing bilateral triac mounted in a standard 6 pin dual-in-line package.

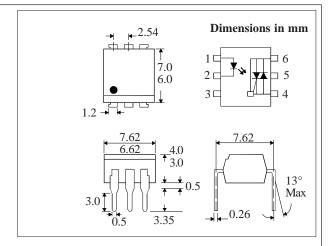
### **FEATURES**

- Options:-10mm lead spread - add G after part no. Surface mount - add SM after part no. Tape&reel - add SMT&R after part no.
- High Isolation Voltage  $(5.3kV_{RMS}, 7.5kV_{PK})$
- Zero Voltage Crossing
- 400V Peak Blocking Voltage
- All electrical parameters 100% tested
- Custom electrical selections available

# APPLICATIONS

- CRTs
- Power Triac Driver
- Motors
- Consumer appliances
- Printers





# ABSOLUTE MAXIMUM RATINGS (25 °C unless otherwise noted)

2111181 - 11111	-55°C-+150°C -40°C-+100°C
Lead Soldering Temperature	260°C
(1.6mm from case for 10 seconds	)

# INPUTDIODE

Forward Current	50mA
Reverse Voltage	6V
Power Dissipation	120mW
(derate linearly 1.41mW/°C a	bove 25°C)

### **OUTPUT PHOTO TRIAC**

Off-State Output Terminal Voltage	400V
Peak Repetitive Surge Current	
(PW=100μs, 120pps)	1A
Power Dissipation	150mW
(derate linearly 1.76mW/°C above 25°C)	

### **POWER DISSIPATION**

Total Power Dissipation	250mW
(derate linearly 2.94mW/°C above 25°C	()

# ISOCOM COMPONENTS 2004 LTD

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DB91048

ELECTRICAL CHARACTERISTICS (  $\rm T_{_{A}} = 25^{\circ}C$  Unless otherwise noted )

	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage $(V_F)$ Reverse Current $(I_R)$		1.2	1.4 10	V μA	$I_{\rm F} = 20 \text{mA}$ $V_{\rm R} = 6 \text{V}$
Output	$\begin{array}{c} \text{Peak Off-state Current ($I_{\text{DRM}}$)} \\ \text{Peak Blocking Voltage ($V_{\text{DRM}}$)} \\ \text{On-state Voltage ($V_{\text{TM}}$)} \end{array}$	400		500 3.0	nA V V	$V_{DRM} = 400 V \text{ (note 1)}$ $I_{DRM} = 500 \text{ nA}$ $I_{TM} = 100 \text{ mA (peak)}$
	Critical rate of rise of off-state Voltage ( dv/dt )	600	1500		V/µs	
Coupled	Input Current to Trigger ( I <sub>FT</sub> )(note 2 )  MOC3040  MOC3041  MOC3042  MOC3043			30 15 10 5	mA mA mA	$V_{TM} = 3V \text{ (note 2)}$
	$\begin{aligned} & \text{Holding Current , either direction (I}_{_{\text{H}}}) \\ & \text{Input to Output Isolation Voltage V}_{_{\text{ISO}}} \end{aligned}$	5300 7500	400		$\begin{array}{c} \mu A \\ V_{_{RMS}} \\ V_{_{PK}} \end{array}$	See note 3 See note 3
Zero Crossing Charact- -eristic	Inhibit Voltage ( $V_{IH}$ ) Leakage in Inhibited State ( $I_{S}$ )			20 500	V μA	$I_F$ = Rated $I_{FT}$ MT1-MT2 Voltage above which device will not trigger $I_F$ = Rated $I_{FT}$ $V_{DRM}$ = Rated $V_{DRM}$
						Off-state

Note 1. Test voltage must be applied within dv/dt rating. Note 2. Guaranteed to trigger at an  $I_F$  value less than or equal to max.  $I_{FT}$ , recommended  $I_F$  lies between Rated  $I_{FT}$  and absolute max.  $I_F$ . Note 3. Measured with input leads shorted together and output leads shorted together.

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