

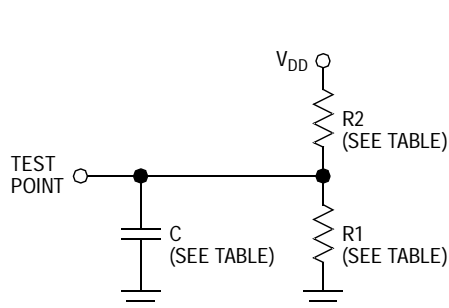
13.4 Operating Temperature Range

Rating ⁽¹⁾	Symbol	Value	Unit
Operating temperature range ⁽²⁾ MC68HC705C8ACB MC68HC705C8ACFB MC68HC705C8ACFS MC68HC705C8ACP MC68HC705C8ACFN MC68HC705C8ACFS	T_A	T_L to T_H – 40 to + 85	°C

1. Voltages referenced to V_{SS}
2. C = Extended temperature range (– 40°C to + 85°C)
P = Plastic dual in-line package (PDIP)
B = Plastic shrink dual in-line package (SDIP)
S = Ceramic dual in-line package (cerdip)
FN = Plastic-leaded chip carrier (PLCC)
FB = Quad flat pack (QFP)
FS = Ceramic-leaded chip carrier (CLCC)

13.5 Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal resistance Plastic dual in-line package (DIP) Ceramic dual in-line package (cerdip) Plastic leaded chip carrier (PLCC) Quad flat pack (QFP) Plastic shrink DIP (SDIP)	θ_{JA}	60 50 70 95 60	°C/W



$V_{DD} = 4.5 \text{ V}$

Pins	R1	R2	C
PA7–PA0 PB7–PB0 PC7–PC0 PD4–PD1	3.26 k Ω	2.38 k Ω	50 pF

$V_{DD} = 3.0 \text{ V}$

Pins	R1	R2	C
PA7–PA0 PB7–PB0 PC7–PC0 PD4–PD1	10.91 k Ω	6.32 k Ω	50 pF
PD7, PD5, PD0	6 k Ω	6 k Ω	200 pF

Figure 13-1. Equivalent Test Load