

300mA Low Drop-out Linear Regulator

Features

- Low Dropout Voltage of 250mV at 300mA
- Guaranteed 300mA Output Current
- Very Low Quiescent Current of about 30µA
- Output Voltage Accuracy of ±2% for 1.2V~3.3V
- Needs only 1µF Capacitor for Stability
- Thermal Shutdown Protection
- Current Limit Protection
- Output Voltage Fast Discharge
- Low-ESR Ceramic Capacitor for Output Stability
- SC-70-4, SC-70-5, SOT-23 and SOT-23-5 packages
- RoHS-compliant and Halogen-free
- High PSRR

Applications

- DVD/CD-ROM/, CD/RW drives
- Wireless Devices
- LCD Modules
- Battery Power Systems
- Card Readers
- XDSL Routers

Description

The APE8865-HF-3 series are low dropout, positive linear regulators with very low quiescent current, and can supply 300mA of output current with a low drop-out voltage of about 250mV.

The APE8865-HF-3 regulator is able to operate with output capacitors as small as $1\mu F$ for stability. As well as current limit protection, the APE8865-HF-3 also offers an on-chip thermal shutdown feature providing protection against overload or conditions where the junction temperature exceeds the specified thermal shutdown temperature.

The APE8865-3 is available with fixed output voltages from 1.2V to 3.3V in 100mV increments.

The APE8865-HF-3 series are available in low-profile, space-saving SC-70-4, SC-70-5, SOT-23 and SOT-23-5 packages.

Typical Application Circuit

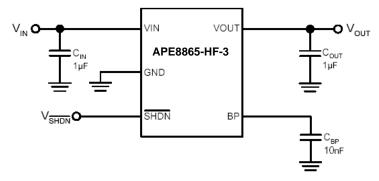


Figure 1. Typical Application Circuit of APE8865-HF-3

Note: If ceramics are used as input or output capacitors, it is recommended that, in order to prevent oscillation, X7R or X5R dielectric capacitors with a minimum value of 1uF are used.

Ordering Information

APE8865xx-yy-HF-3TR

N: RoHS-compliant halogen-free SOT-23
NL: RoHS-compliant halogen-free SOT-23
NR: RoHS-compliant halogen-free SOT-23
Y5: RoHS-compliant halogen-free SOT-23-5
U4: RoHS-compliant halogen-free SC-70-4

U5: RoHS-compliant halogen-free SC-70-5

Fixed Output Voltage Options Packing TR: Products are shipped on tape and reel, 3000pcs/reel.

yy = 12 : 1.2V 15 : 1.5V

in 100mV increments through

33:3.3V

The device is rated MSL3 for moisture sensitivity, and the reel is packed in a moisture barrier bag.

Absolute Maximum Ratings (at T_A=25°C)

Input Voltage (VIN) ----- 6V SHDN Voltage (V_{SHDN}) ----- GND - 0.3V to VIN + 0.3V Power Dissipation (SOT-23, SOT-23-5) ----- 0.4W (SC-70-4) ----- 0.23W (SC-70-5) ----- 0.23W Maximum Junction Temperature ----- 150°C Maximum Thermal Resistance, Junction-ambient: SOT-23, SOT-23-5 ----- 250°C/W SC-70-4/5 ----- 600°C/W

Recommended Operating Conditions

Input Voltage (VIN) ----- 2.8 to 5.5V Operating Junction Temperature Range (T_J) ----- -40°C to +125°C

Electrical Specifications

(V_{IN}=V_{OUT}+1V or V_{IN}=2.8V whichever is greater, C_{IN}=1uF, C_{OUT}=1uF, T_A=25°C, unless otherwise specified)

Parameter	SYM	TES	TEST CONDITION		TYP	MAX	UNITS
Output Voltage Accuracy	ΔV_{OUT}	I _O = 1mA		-2	-	2	%
Current Limit	I _{LIMIT}	$R_{Load}=1\Omega$		300	-	-	mA
Quiescent Current	Ι _Q	$I_O = 0 mA$		-	30	55	μΑ
			1.2V < V _{OUT} < 2.0V	-	1000	-	
Dropout Voltage (Note 1)	V_{DROP}	I _O =300mA	$2.0V < V_{OUT} < 2.8V$		350	-	mV
			2.8V < V _{OUT} < 3.3V	-	250	-	
Line Regulation	ΔV_{LINE}	I _O =1mA, V _{IN} =V _{OUT} +1V to 5V		-	1	5	mV
Load Regulation (Note 2)	ΔV_{LOAD}	I _O =0mA to 300mA		•	6	20	mV
Ripple Rejection	PSRR	$I_o=1$ mA, $C_{OUT}=1$ uF, $f_{RIPPLE}=100$ Hz		-	-73	-	dB
Nipple Rejection	FORK		$f_{RIPPLE} = 10kHz$	-	-50	-	uБ
Temperature Coefficient	TC	$I_{OUT} = 1mA$,	$V_{IN} = 5V$	•	50	-	ppm/ °C
Thermal Shutdown Temperature	TSD			-	160	-	°C
Thermal Shutdown Hysteresis	ΔTSD			-	25	-	°C
Shutdown Pin Current	I _{SHDN}			•	-	0.1	μA
Shutdown Pin Voltage (ON)	V _{SHDN(ON)}			1.4	-	-	V
Shutdown Pin Voltage (OFF)	V _{SHDN} (OFF)			-	-	0.4	V
Shutdown Exit Delay Time	ΔΤ	$C_{BP}=0.1uF, 0$	C _{OUT} =1uF, I _{OUT} =30mA	-	150	-	μs

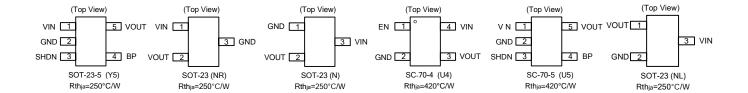
Note 1 : The dropout voltage is defined as V $_{\text{IN}}$ -V $_{\text{OUT}}$, which is measured when V $_{\text{OUT}}$ drops about 100mV.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED. APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

Note 2: Regula ion is measured at a constant junc ion temperature by using 30ms current pulse and load regulation in the load range from 0mA to 300mA.

Pin Configuration



Pin Descriptions

PIN SYMBOL	PIN DESCRIPITON
VIN	Power is supplied to the device through this pin and requires an input filter capacitor. In general, an input capacitor in the range of 1µF to 10µF is sufficient.
VOUT	The output supplies power to loads. The output capacitor is required to provide a stable output voltage. The APE8865-HF-3 is stable with an output capacitor of 1µF or greater. A larger output capacitor will be required for applications with large transient loads to limit peak voltage transients, and can also reduceoutput noise, improve stability and PSRR.
GND	Common ground pin
BP	Reference-noise bypass (the bypass capacitor should be at least 1nF)
SHDN	Chip-enable (active-high)

Block Diagram

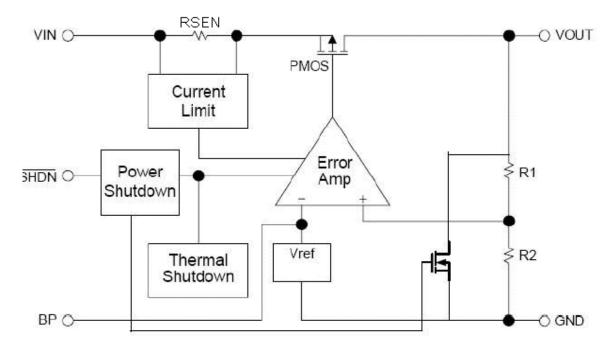


Figure 2. Block diagram of APE8865-3 with auto-discharge



Typical Performance Characteristics

Fig. 1 Dropout Voltage vs. Output Current

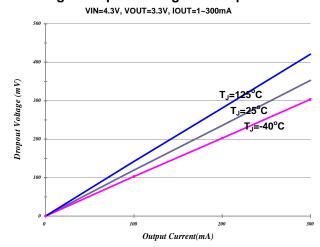


Fig.2 Dropout Voltage vs. Temperature

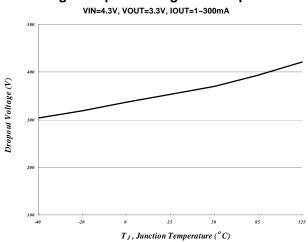


Fig.3 Quiescent Current vs. Temperature

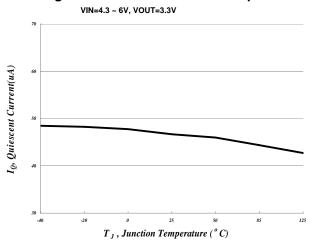


Fig.4 Quiescent Current vs Input Voltage

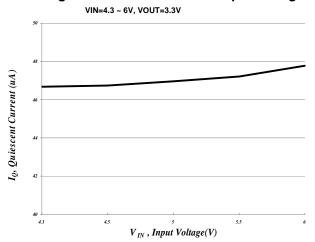


Fig.5 Line Regulation

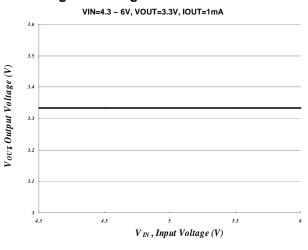
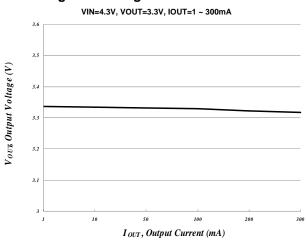


Fig.6 Load Regulation





Typical Performance Characteristics (cont.)

Fig.7 Load Transient

VIN=4.3V, VOUT=1.8V, ELOAD=10~300mA



Fig.8 Current Limit vs. Temperature

VIN=4.3V, VOUT=3.3V, ELOAD= Short GND

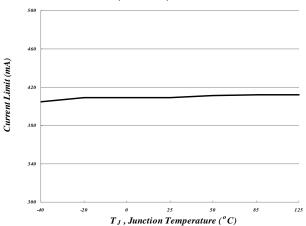


Fig.9 Power ON

VIN=4.3V, VOUT=3.3V, ROUT=11 Ω



Fig.10 Power OFF

VIN=4.3V, VOUT=3.3V, ROUT=11 Ω



Fig.11 Enable ON

VIN=4.3V, VOUT=3.3V, ROUT=11 Ω



Fig. 12 Enable OFF

VIN=4.3V, VOUT=3.3V, ROUT=11 Ω



Application Description

The APE8865-3 series are low dropout linear regulators that can provide 300mA output current with a drop-out voltage of about 300mV. Also, current limit and on-chip thermal shutdown features provide protection against any combination of overload or junction temperature that exceeds the shutdown temperature.

1. Output and Input Capacitor

The APE8865-3 regulator is designed to be stable with a wide range of output capacitors. The ESR of the output capacitor affects stability. Larger values of the output capacitor decrease the peak deviations and provide improved transient response for larger current changes.

The various capacitor types (aluminum, ceramic, tantalum) have different characteristics such as temperature and voltage coefficients. All ceramic capacitors are manufactured with a variety of dielectrics, each with different behavior across temperature and applications. Common dielectrics used are X5R, X7R and Y5V. It is recommended to use 1uF X5R or X7R dielectric ceramic capacitors with $30m\Omega$ to $50m\Omega$ ESR range between device outputs to ground for transient stability.

The APE8865-3 is designed to be stable with low ESR ceramic capacitors, and higher values of capacitors and ESR can improve output stability.

So the ESR of the output capacitor is very important because it generates a zero to provide phase lead for loop stability.

There are no requirements for the ESR on the input capacitor, but its voltage and temperature coefficient have to be considered for the device application environment.

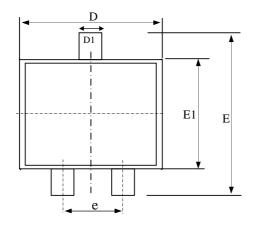
2. Protection Features

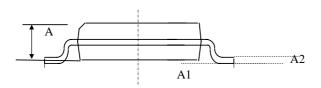
In order to prevent overloading or a thermal condition from damaging the device, the APE8865-3 regulator has internal thermal and current-limiting functions designed to protect the device. It will rapidly shut off the internal P-channel MOSFET pass element during overloading or an over-temperature condition.

3. Thermal Consideration

The power handling capability of the device is limited by the maximum operation junction temperature (125°C). The power dissipated by the device can be estimated by PD = IOUT \times (VIN-VOUT). This power dissipation must be lower than the maximum power dissipation listed in the "Absolute Maximum Ratings" section.

Package Dimensions: SOT-23

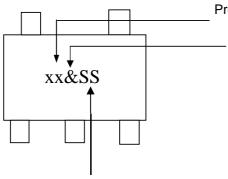




SYMBOLS	Millimeters			
	MIN	NOM	MAX	
A	1.00	1.15	1.30	
A1	0.00		0.10	
A2	0.10	0.15	0.25	
D1	0.30	0.40	0.50	
e	1.70	2.00	2.30	
D	2.70	2.90	3.10	
Е	2.40	2.65	3.00	
E1	1.40	1.50	1.60	

- 1. All dimensions are in millimeters.
- 2. Dimensions do not include mold protrusions.

Marking Information



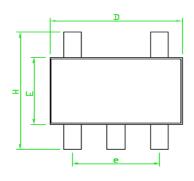
SS = Date/lot code
For details on translating this
code to YYWW, please
contact APEC USA

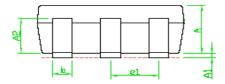
Product: RL = APE8865N-HF-3, RI = APE8865NR-HF-3rI = APE8865NL-HF-3

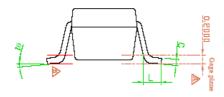
Fixed output voltage - see table below:

V _{OUT}	Identification Code	V _{OUT}	Identification Code
1.2V	2	2.4V	Т
1.5V	5	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	D
1.8V	А	2.8V	Е
1.9V	а	2.9V	h
2.0V	е	3.0V	Ι
2.1V	В	3.1V	Х
2.2V	С	3.2V	U
2.3V	V	3.3V	I

Package Dimensions: SOT-23-5







SYMBOLS	Millimeters			
	MIN	NOM	MAX	
A	1.00	1.10	1.30	
A1	0.00		0.10	
A2	0.70	0.80	0.90	
b	0.35	0.40	0.50	
C	0.10	0.15	0.25	
D	2.70	2.90	3.10	
Е	1.50	1.60	1.80	
e		1.90(TYP)		
Н	2.60 2.80 3.		3.00	
L	0.37			
θ1	1° 5° 9		9°	
e2		0.95(TYP)		

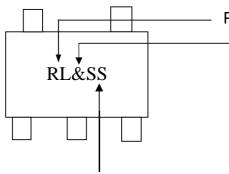
Note 1: Dimensions exclude mold flash protrusions or gate burrs.

Note 2: Tolerance ± 0.1000 mm (4mil) unless otherwise specified.

Note 3: Coplanarity: 0.1000 mm

Note 4: Dimension L Is measured in gage plane.

Marking Information

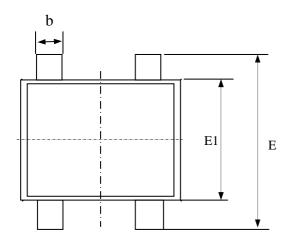


SS = Date/lot code For details on translating this code to YYWW, please contact APEC USA

Product: RL = APE8865
Fixed output voltage - see table below:

V _{OUT}	Identification Code	V _{OUT}	Identification Code
1.2V	2	2.4V	Т
1.5V	5	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	D
1.8V	Α	2.8V	Е
1.9V	а	2.9V	h
2.0V	е	3.0V	Н
2.1V	В	3.1V	Х
2.2V	С	3.2V	U
2.3V	V	3.3V	I

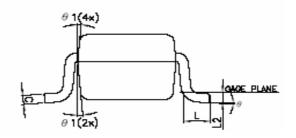
Package Dimensions: SC-70-4



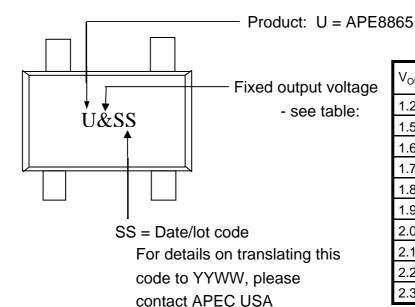
	-		D			
			c		1	
¥						R
		,		У	01(2	×) &

- All dimensions are in millimeters.
- 2. Dimensions do not include mold protrusions.

	Millimeters			
SYMBOLS	MIN	NOM	MAX	
A	0.80		1.10	
A1	0.00		0.10	
A2	0.70	0.90	1.00	
b	0.25	0.30	0.40	
С	0.10		0.26	
D	1.80	2.00	2.20	
E1	1.15	1.25	1.35	
Е	1.80	2.10	2.40	
e		1.30		
L	0.15	0.30	0.45	
L2		0.15		
у			0.10	
θ	0°		8°	
θ1	4°		12°	



Marking Information



V_{OUT}	Identification	V_{OUT}	Identification
V OUT	Code		Code
1.2V	В	2.4V	Т
1.5V	С	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	W
1.8V	D	2.8V	G
1.9V	а	2.9V	h
2.0V	е	3.0V	Ι
2.1V	b	3.1V	Х
2.2V	0	3.2V	U
2.3V	٧	3.3V	I

MAX 1.10

0.10

1.00

0.40

0.26

2.20

1.35

2.40

0.45

0.10

8°

12°

Millimeters

NOM

0.90

0.30

0.38

2.00

1.25

2.10

1.30

0.30

0.15

MIN

0.80

0.70

0.25

0.10

1.80

1.15

1.80

0.15

0°

4°

SYMBOLS

A1

A2

b

b1

C

D

E1 E

e

L L2

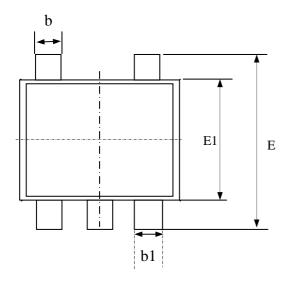
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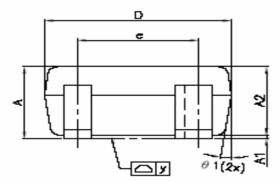
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Package Dimensions: SC70-5

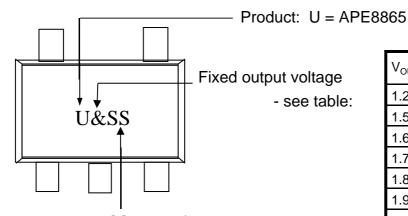




θ 1(4×)	
	QUE PLANE
θ 1(2x)	L 2

- 1. All dimensions are in millimeters.
- 2. Dimensions do not include mold protrusions.

Marking Information



SS = Date/lot code
For details on translating this
code to YYWW, please
contact APEC USA

	Identification		Identification
V_{OUT}	Code	V_{OUT}	Code
1.2V	В	2.4V	Т
1.5V	С	2.5V	F
1.6V	S	2.6V	f
1.7V	Χ	2.7V	W
1.8V	D	2.8V	G
1.9V	а	2.9V	h
2.0V	е	3.0V	Η
2.1V	b	3.1V	Х
2.2V	0	3.2V	J
2.3V	V	3.3V	I