100 mA Positive Voltage Regulators

The MC78L00A Series of positive voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100 mA. Like their higher powered MC7800 and MC78M00 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the MC78L00 devices in many applications.

These devices offer a substantial performance advantage over the traditional zener diode–resistor combination, as output impedance and quiescent current are substantially reduced.

Features

- Wide Range of Available, Fixed Output Voltages
- Low Cost
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required
- Complementary Negative Regulators Offered (MC79L00A Series)
- Pb-Free Packages are Available
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes

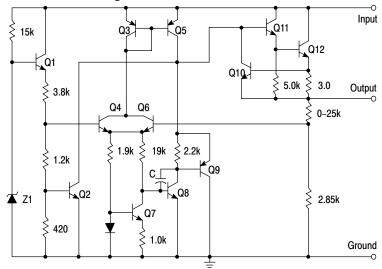


Figure 1. Representative Schematic Diagram

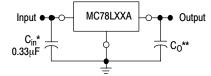


Figure 2. Standard Application

A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

- *C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O is not needed for stability; however, it does improve transient response.



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TO-92 P SUFFIX CASE 029

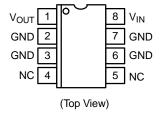
Pin: 1. Output 2. Ground 3. Input



SOIC-8* D SUFFIX CASE 751

*SOIC-8 is an internally modified SO-8 package. Pins 2, 3, 6, and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipation capability when appropriately mounted on a printed circuit board. SOIC-8 conforms to all external dimensions of the standard SO-8 package.

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 13 of this data sheet.

MAXIMUM RATINGS ($T_A = +125^{\circ}C$, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (2.6 V-8.0 V) (12 V-18 V)	VI	30 35	Vdc
(12 V – 18 V) (24 V)		40	
Storage Temperature Range	T _{stg}	-65 to +150	°C
Operating Junction Temperature Range	T_J	0 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS (V_I = 10 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40° C < T_J < +125 $^{\circ}$ C (for MC78LXXAB, NCV78L05A), 0° C < T_J < +125 $^{\circ}$ C (for MC78LXXAC), unless otherwise noted.)

		MC78L05AC, AB, NCV78L05A			
Characteristics	Symbol	Min	Тур Мах		Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	4.8	5.0	5.2	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA)	Reg _{line}				mV
7.0 $Vdc \le V_1 \le 20 Vdc$ 8.0 $Vdc \le V_1 \le 20 Vdc$		-	55 45	150 100	
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		11 5.0	60 30	mV
Output Voltage $(7.0 \text{ Vdc} \le \text{V}_{\text{I}} \le 20 \text{ Vdc}, \ 1.0 \text{ mA} \le \text{I}_{\text{O}} \le 40 \text{ mA})$ $(\text{V}_{\text{I}} = 10 \text{ V}, \ 1.0 \text{ mA} \le \text{I}_{\text{O}} \le 70 \text{ mA})$	Vo	4.75 4.75	- -	5.25 5.25	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		3.8	6.0 5.5	mA
Input Bias Current Change (8.0 Vdc \leq V _I \leq 20 Vdc) (1.0 mA \leq I _O \leq 40 mA)	$\Delta I_{ m IB}$	- -	- -	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	-	40	_	μV
Ripple Rejection ($I_O = 40 \text{ mA}$, $f = 120 \text{ Hz}$, $8.0 \text{ Vdc} \le V_I \le 18 \text{ V}$, $T_J = +25 ^{\circ}\text{C}$)	RR	41	49	-	dB
Dropout Voltage ($T_J = +25^{\circ}C$)	$V_I - V_O$	-	1.7	-	Vdc

NOTE: NCV78L05A: $T_{low} = -40^{\circ}C$, $T_{high} = +125^{\circ}C$. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

ELECTRICAL CHARACTERISTICS (V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40° C < T_J < +125 $^{\circ}$ C (for MC78LXXAB), 0 $^{\circ}$ C < T_J < +125 $^{\circ}$ C (for MC78LXXAC), unless otherwise noted.)

		М	MC78L08AC, AB		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	7.7	8.0	8.3	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$	Reg _{line}				mV
10.5 $Vdc \le V_1 \le 23 Vdc$ 11 $Vdc \le V_1 \le 23 Vdc$		-	20 12	175 125	
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}	_ _	15 8.0	80 40	mV
Output Voltage $(10.5 \text{ Vdc} \le \text{V}_1 \le 23 \text{ Vdc}, 1.0 \text{ mA} \le \text{I}_O \le 40 \text{ mA})$ $(\text{V}_1 = 14 \text{ V}, 1.0 \text{ mA} \le \text{I}_O \le 70 \text{ mA})$	Vo	7.6 7.6	- -	8.4 8.4	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}	_ _	3.0	6.0 5.5	mA
Input Bias Current Change (11 Vdc \leq V _I \leq 23 Vdc) (1.0 mA \leq I _O \leq 40 mA)	Δl _{IB}	- -	- -	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	-	60	-	μV
Ripple Rejection (I_O = 40 mA, f = 120 Hz, 12 V \leq V _I \leq 23 V, T _J = +25°C)	RR	37	57	-	dB
Dropout Voltage ($T_J = +25^{\circ}C$)	$V_I - V_O$	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40° C < T_J < +125 $^{\circ}$ C (for MC78LXXAB), 0°C < T_J < +125 $^{\circ}$ C (for MC78LXXAC), unless otherwise noted.)

		М	C78L09AC,		
Characteristics	Symbol	Min Typ		Max	Unit
Output Voltage (T _J = +25°C)	Vo	8.6	9.0	9.4	Vdc
Line Regulation $ (T_J = +25^{\circ}\text{C}, I_O = 40 \text{ mA}) $ $ 11.5 \text{ Vdc} \le V_I \le 24 \text{ Vdc} $ $ 12 \text{ Vdc} \le V_I \le 24 \text{ Vdc} $	Reg _{line}	- -	20 12	175 125	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}	- -	15 8.0	90 40	mV
Output Voltage $(11.5 \text{ Vdc} \leq \text{V}_{\text{I}} \leq 24 \text{ Vdc}, 1.0 \text{ mA} \leq \text{I}_{\text{O}} \leq 40 \text{ mA})$ $(\text{V}_{\text{I}} = 15 \text{ V}, 1.0 \text{ mA} \leq \text{I}_{\text{O}} \leq 70 \text{ mA})$	Vo	8.5 8.5	- -	9.5 9.5	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}	- -	3.0	6.0 5.5	mA
Input Bias Current Change (11 Vdc \leq V _I \leq 23 Vdc) (1.0 mA \leq I _O \leq 40 mA)	Δl_{IB}	- -	- -	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	-	60	-	μV
Ripple Rejection ($I_O = 40$ mA, f = 120 Hz, 13 V \leq V _I \leq 24 V, T _J = +25°C)	RR	37	57	-	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 19 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40° C < T_J < +125 $^{\circ}$ C (for MC78LXXAB), 0 $^{\circ}$ C < T_J < +125 $^{\circ}$ C (for MC78LXXAC), unless otherwise noted.)

		М	MC78L12AC, AB		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	11.5	12	12.5	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$	Reg _{line}				mV
14.5 $Vdc \le V_1 \le 27 Vdc$ 16 $Vdc \le V_1 \le 27 Vdc$		-	120 100	250 200	
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}	-	20 10	100 50	mV
Output Voltage $(14.5 \text{ Vdc} \le \text{V}_1 \le 27 \text{ Vdc}, \ 1.0 \text{ mA} \le \text{I}_0 \le 40 \text{ mA})$ $(\text{V}_1 = 19 \text{ V}, \ 1.0 \text{ mA} \le \text{I}_0 \le 70 \text{ mA})$	Vo	11.4 11.4	- -	12.6 12.6	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}	- -	4.2	6.5 6.0	mA
Input Bias Current Change (16 Vdc \leq V _I \leq 27 Vdc) (1.0 mA \leq I _O \leq 40 mA)	$\Delta l_{ m lB}$	- -		1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	-	80	-	μV
Ripple Rejection (I_O = 40 mA, f = 120 Hz, 15 V \leq V _I \leq 25 V, T _J = +25°C)	RR	37	42	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	$V_I - V_O$	_	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 23 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40°C < T_J < +125°C (for MC78LXXAB), 0°C < T_J < +125°C (for MC78LXXAC), unless otherwise noted.)

		M	C78L15AC,	AB	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	14.4	15	15.6	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ $17.5 \text{ Vdc} \le V_I \le 30 \text{ Vdc}$	Reg _{line}	_	130	300	mV
$20 \text{ Vdc} \le \text{V}_{\text{I}} \le 30 \text{ Vdc}$		_	110	250	
Load Regulation $ (T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA}) \\ (T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA}) $	Reg _{load}	_ _	25 12	150 75	mV
Output Voltage $(17.5 \text{ Vdc} \le \text{V}_{\text{I}} \le 30 \text{ Vdc}, 1.0 \text{ mA} \le \text{I}_{\text{O}} \le 40 \text{ mA})$ $(\text{V}_{\text{I}} = 23 \text{ V}, 1.0 \text{ mA} \le \text{I}_{\text{O}} \le 70 \text{ mA})$	Vo	14.25 14.25	- -	15.75 15.75	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}	_ _	4.4 -	6.5 6.0	mA
Input Bias Current Change (20 Vdc \leq V _I \leq 30 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI_{IB}	- -	- -	1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	_	90	-	μV
Ripple Rejection (I_O = 40 mA, f = 120 Hz, 18.5 V \leq V _I \leq 28.5 V, T _J = +25°C)	RR	34	39	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	V _I – V _O	_	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 27 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, 0°C < T_J < +125°C, unless otherwise noted.)

			MC78L18A	<u> </u>	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	17.3	18	18.7	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 21.4 $Vdc \le V_I \le 33 \text{ Vdc}$ 20.7 $Vdc \le V_I \le 33 \text{ Vdc}$ 22 $Vdc \le V_I \le 33 \text{ Vdc}$	Reg _{line}	-	45	325	mV
21 $Vdc \le V_1 \le 33 Vdc$		-	35	275	
Load Regulation $ (T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA}) $ $ (T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA}) $	Reg _{load}		30 15	170 85	mV
Output Voltage $ (21.4 \text{ Vdc} \le V_l \le 33 \text{ Vdc}, \ 1.0 \text{ mA} \le I_O \le 40 \text{ mA}) $ $ (20.7 \text{ Vdc} \le V_l \le 33 \text{ Vdc}, \ 1.0 \text{ mA} \le I_O \le 40 \text{ mA}) $ $ (V_l = 27 \text{ V}, \ 1.0 \text{ mA} \le I_O \le 70 \text{ mA}) $ $ (V_l = 27 \text{ V}, \ 1.0 \text{ mA} \le I_O \le 70 \text{ mA}) $	Vo	17.1 17.1	-	18.9 18.9	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}	_ _	3.1 -	6.5 6.0	mA
Input Bias Current Change (22 Vdc \leq V $_{I}$ \leq 33 Vdc) (21 Vdc \leq V $_{I}$ \leq 33 Vdc) (1.0 mA \leq I $_{O}$ \leq 40 mA)	Δl_{IB}			1.5 0.1	mA
Output Noise Voltage $(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$	V _n	_	150	-	μV
Ripple Rejection (I_O = 40 mA, f = 120 Hz, 23 V ≤ V_I ≤ 33 V, T_J = +25°C)	RR	33	48	-	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	_	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 33 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, 0°C < T_J < +125°C, unless otherwise noted.)

			MC78L24AC		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	23	24	25	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$	Reg _{line}				mV
$27.5 \text{ Vdc} \le V_1 \le 38 \text{ Vdc}$		-	-	-	
28 $Vdc \le V_1 \le 80 Vdc$ 27 $Vdc \le V_1 \le 38 Vdc$		_	50 60	300 350	
Load Regulation	Reg _{load}				mV
$(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$		_	40	200	
$(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$		-	20	100	
Output Voltage (28 Vdc \leq V ₁ \leq 38 Vdc, 1.0 mA \leq I _O \leq 40 mA)	Vo				Vdc
$(27 \text{ Vdc} \le V_1 \le 38 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 40 \text{ mA})$ $(28 \text{ Vdc} \le V_1 = 33 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 70 \text{ mA})$		22.8	_	25.2	
$(27 \text{ Vdc} \le V_1 \le 33 \text{ Vdc}, 1.0 \text{ mA} \le I_0 \le 70 \text{ mA})$		22.8	_	25.2	
Input Bias Current	I _{IB}				mA
$(T_J = +25^{\circ}C)$		_	3.1	6.5	
$(T_J = +125^{\circ}C)$		-	-	6.0	
Input Bias Current Change (28 Vdc \leq V ₁ \leq 38 Vdc)	ΔI_{IB}			1.5	mA
$(20 \text{ Vac} \le V_1 \le 30 \text{ Vac})$ $(1.0 \text{ mA} \le I_0 \le 40 \text{ mA})$		_	_	0.1	
Output Noise Voltage	V _n	_	200	_	μV
$(T_A = +25^{\circ}C, 10 \text{ Hz} \le f \le 100 \text{ kHz})$					
Ripple Rejection ($I_O = 40 \text{ mA}$, f = 120 Hz, 29 V \leq V _I \leq 35 V, T _J = +25°C)	RR	31	45	_	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	-	1.7	-	Vdc

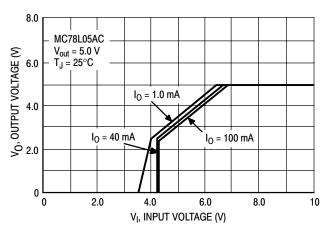


Figure 3. Dropout Characteristics

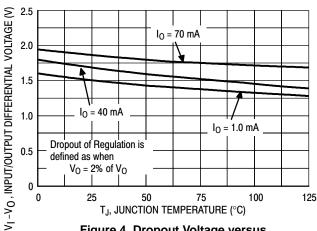


Figure 4. Dropout Voltage versus Junction Temperature

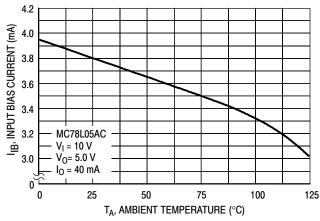


Figure 5. Input Bias Current versus Ambient Temperature

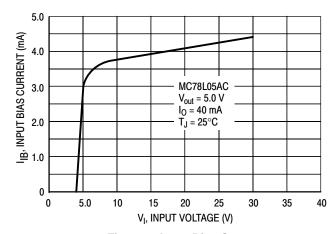


Figure 6. Input Bias Current versus Input Voltage

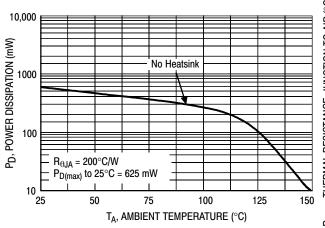


Figure 7. Maximum Average Power Dissipation versus Ambient Temperature – TO-92 Type Package

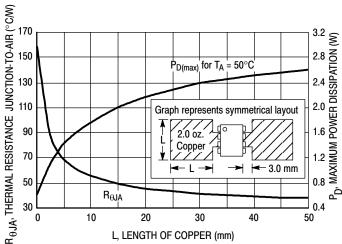


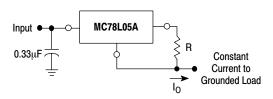
Figure 8. SOIC-8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

APPLICATIONS INFORMATION

Design Considerations

The MC78L00A Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. The



The MC78L00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC78L05C is chosen in this application. Resistor R determines the current as follows:

$$I_0 = \frac{5.0 \text{ V}}{\text{B}} + I_{\text{B}}$$

I_{IB} = 3.8 mA over line and load changes

For example, a 100 mA current source would require R to be a 50 Ω , 1/2 W resistor and the output voltage compliance would be the input voltage less 7 V.

Figure 9. Current Regulator

input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.

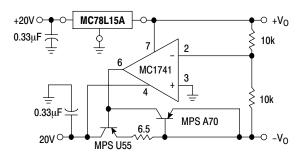


Figure 10. \pm 15 V Tracking Voltage Regulator

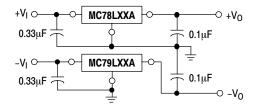


Figure 11. Positive and Negative Regulator

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
MC78L05ABD			SOIC-8	98 Units/Rail
MC78L05ABDG			SOIC-8	98 Units/Rail
MC78L05ABDR2			SOIC-8	2500 Tape & Reel
MC78L05ABDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
NCV78L05ABDR2*			SOIC-8	2500 Tape & Reel
MC78L05ABP			TO-92	2000 Units/Bag
MC78L05ABPG		$T_{.1} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Units/Bag
MC78L05ABPRA		.,	TO-92	2000 Tape & Reel
MC78L05ABPRAG			TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ABPRE			TO-92	2000 Tape & Reel
MC78L05ABPREG			TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ABPRM	1		TO-92	2000 Ammo Pack
MC78L05ABPRMG			TO-92 (Pb-Free)	2000 Ammo Pack
MC78L05ACD	=		SOIC-8	98 Units/Rail
MC78L05ACDG	5.0 V		SOIC-8 (Pb-Free)	98 Units/Rail
MC78L05ACDR2	=		SOIC-8	2500 Tape & Reel
MC78L05ACDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L05ACP	=		TO-92	2000 Units/Bag
MC78L05ACPG			TO-92 (Pb-Free)	2000 Units/Bag
MC78L05ACPRA	=		TO-92	2000 Tape & Reel
MC78L05ACPRAG		$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ACPRE			TO-92	2000 Tape & Reel
MC78L05ACPREG			TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ACPRM			TO-92	2000 Ammo Pack
MC78L05ACPRMG			TO-92 (Pb-Free)	2000 Ammo Pack
MC78L05ACPRP]		TO-92	2000 Ammo Pack
MC78L05ACPRPG			TO-92 (Pb-Free)	2000 Ammo Pack

^{*}NCV78L05A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
MC78L08ABD			SOIC-8	98 Units/Rail
MC78L08ABDG			SOIC-8 (Pb-Free)	98 Units/Rail
MC78L08ABDR2			SOIC-8	2500 Tape & Reel
MC78L08ABDR2G		$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L08ABP			TO-92	2000 Units/Bag
MC78L08ABPRA			TO-92	2000 Tape & Reel
MC78L08ABPRP			TO-92	2000 Ammo Pack
MC78L08ACD			SOIC-8	98 Units/Rail
MC78L08ACDG	8.0 V		SOIC-8	98 Units/Rail
MC78L08ACDR2	0.0 V		SOIC-8	2500 Tape & Reel
MC78L08ACP			TO-92	2000 Units/Bag
MC78L08ACPG			TO-92 (Pb-Free)	2000 Units/Bag
MC78L08ACPRA		$T_J = 0^\circ$ to +125°C	TO-92	2000 Tape & Reel
MC78L08ACPRAG			TO-92 (Pb-Free)	2000 Tape & Reel
MC78L08ACPRE			TO-92	2000 Tape & Reel
MC78L08ACPRP			TO-92	2000 Ammo Pack
MC78L08ACPRPG			TO-92 (Pb-Free)	2000 Ammo Pack
MC78L09ABD			SOIC-8	98 Units/Rail
MC78L09ABDG			SOIC-8 (Pb-Free)	98 Units/Rail
MC78L09ABDR2		T 400 to 140500	SOIC-8	2500 Tape & Reel
MC78L09ABPRA		$T_J = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92	2000 Tape & Reel
MC78L09ABPRP			TO-92	2000 Ammo Pack
MC78L09ABDG			SOIC-8 (Pb-Free)	98 Units/Rail
MC78L09ACD			SOIC-8	98 Units/Rail
MC78L09ACDG	9.0 V		SOIC-8 (Pb-Free)	98 Units/Rail
MC78L09ACDR2			SOIC-8	2500 Tape & Reel
MC78L09ACDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L09ACP		$T_J = 0^\circ$ to +125°C	TO-92	2000 Units/Bag
MC78L09ACPG			TO-92 (Pb-Free)	2000 Units/Bag
MC78L09ACDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L09ACPG			TO-92 (Pb-Free)	2000 Units/Bag

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
MC78L12ABD			SOIC-8	98 Units/Rail
MC78L12ABDR2			SOIC-8	2500 Tape & Reel
MC78L12ABP		$T_J = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92	2000 Units/Bag
MC78L12ABPRP			TO-92	2000 Ammo Pack
NCV78L12ABDR2*			SOIC_8	2500 Tape & Reel
MC78L12ACD			SOIC-8	98 Units/Rail
MC78L12ACDR2			SOIC-8	2500 Tape & Reel
MC78L12ACP			TO-92	2000 Units/Bag
MC78L12ACPRA			TO-92	2000 Tape & Reel
MC78L12ACPRAG	12 V		TO-92 (Pb-Free)	2000 Tape & Reel
MC78L12ACPRE			TO-92	2000 Tape & Reel
MC78L12ACPRM		T 00 / 10500	TO-92	2000 Ammo Pack
MC78L12ACPRMG		$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Ammo Pack
MC78L12ACPRP			TO-92	2000 Ammo Pack
MC78L12ACPRPG			TO-92 (Pb-Free)	2000 Ammo Pack
MC78L12ACDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L12ACPG			TO-92 (Pb-Free)	2000 Units/Bag
MC78L15ABD			SOIC-8	98 Units/Rail
MC78L15ABDR2			SOIC-8	2500 Tape & Reel
MC78L15ABDR2G			SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L15ABP		$T_J = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92	2000 Units/Bag
MC78L15ABPRA			TO-92	2000 Tape & Reel
MC78L15ABPRP			TO-92	2000 Ammo Pack
MC78L15ABPRPG	15 V		TO-92 (Pb-Free)	2000 Ammo Pack
MC78L15ACD			SOIC-8	98 Units/Rail
MC78L15ACDR2			SOIC-8	2500 Tape & Reel
MC78L15ACP			TO-92	2000 Units/Bag
MC78L15ACPRA		$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92	2000 Tape & Reel
MC78L15ACPRAG			TO-92 (Pb-Free)	2000 Tape & Reel
MC78L15ACPRP			TO-92	2000 Ammo Pack

^{*}NCV78L12A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
MC78L18ABP	40.1/	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92	2000 Units/Bag
MC78L18ACP	18 V		TO-92	2000 Units/Bag
MC78L18ACPG			TO-92 (Pb-Free)	2000 Units/Bag
MC78L18ACPRA	18 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92	2000 Tape & Reel
MC78L18ACPRM			TO-92	2000 Ammo Pack
MC78L18ACPRP			TO-92	2000 Ammo Pack
MC78L24ABP		$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92	2000 Units/Bag
MC78L24ACP			TO-92	2000 Units/Bag
MC78L24ACPG	24.1/		TO-92 (Pb-Free)	2000 Units/Bag
MC78L24ACPRA	24 V	$T_J = 0^\circ$ to +125°C	TO-92	2000 Tape & Reel
MC78L24ACPRP			TO-92	2000 Ammo Pack
MC78L24ACPRPG			TO-92 (Pb-Free)	2000 Ammo Pack

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS

SOIC-8 D SUFFIX CASE 751





xx = 05, 08, 09, 12, or 15

A = Assembly Location

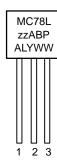
= Wafer Lot

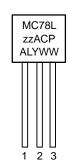
Y = Year

W = Work Week

B, C = Temperature Range

TO-92 P SUFFIX CASE 029





zz = 05, 08, 09, 12, 15, 18 or 24

A = Assembly Location

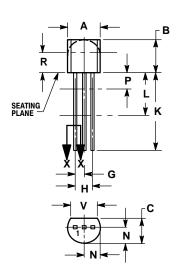
L = Wafer Lot

Y = Year

WW = Work Week

PACKAGE DIMENSIONS

TO-92 (TO-226) P SUFFIX CASE 29-11 **ISSUE AL**

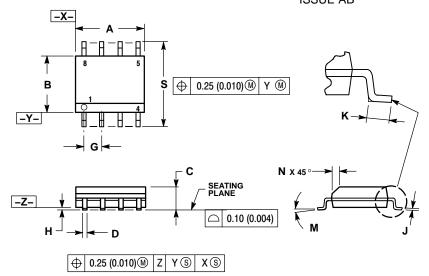




- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P	-	0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

SOIC-8 NB **D SUFFIX** CASE 751-07 **ISSUE AB**

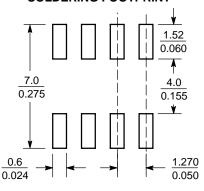


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
 DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION. 751–01 THRU 751–06 ARE OBSOLETE. NEW STANDARD IS 751–07.

	MILLIN	IETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
Η	0.10	0.25	0.004	0.010
7	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
М	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

SOLDERING FOOTPRINT*



 $\left(\frac{\text{mm}}{\text{inches}}\right)$ SCALE 6:1

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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