## VS-40CPQ0...PbF Series, VS-40CPQ0...-N3 Series

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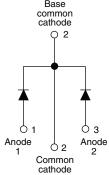
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HALOGEN

FREE

## Schottky Rectifier, 2 x 20 A





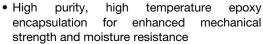
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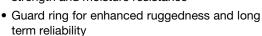
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Com	3 Anode 2 nmon node	

PRODUCT SUMMARY	
Package	TO-247AC
I <sub>F(AV)</sub>	2 x 20 A
$V_{R}$	35 V, 40 V, 45 V
V <sub>F</sub> at I <sub>F</sub>	0.43 V
I <sub>RM</sub> max.	150 mA at 125 °C
$T_J$ max.	150 °C
Diode variation	Common cathode
E <sub>AS</sub>	27 mJ

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Very low forward voltage drop
- · High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The VS-40CPQ... center tap Schottky rectifier has been optimized for very low forward voltage drop with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	40	Α						
$V_{RRM}$		35 to 45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	3500	Α						
V <sub>F</sub>	20 Apk, T <sub>J</sub> = 125 °C (per leg)	0.43	V						
T <sub>J</sub>		- 55 to 150	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS- 40CPQ035PbF	VS- 40CPQ035-N3	VS- 40CPQ040PbF	VS- 40CPQ040-N3	VS- 40CPQ045PbF	VS- 40CPQ045-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	35	35	40	40	45	45	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	35	40	40	45	45	V		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 120 °C, rectangular waveform		40				
Maximum peak one cycle non-repetitive surge current per leg	l=	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	3500	Α			
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	430				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 4 A, L = 3.4 mH		27	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by $T_J$ maxim	o in 1 μs um V <sub>A</sub> = 1.5 x V <sub>R</sub> typical	4	Α			



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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		20 A	T <sub>.1</sub> = 25 °C	0.49	V			
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 C	0.59				
	V <sub>FM</sub> ('')	20 A	T <sub>.1</sub> = 125 °C	0.43				
		40 A	1J = 125 C	0.56				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	4	A			
See fig. 2		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nated v <sub>R</sub>	150	mA			
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		1850	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.5	nΗ			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs			

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANI	THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C				
Maximum thermal resistance, junction to case per leg		$R_{thJC}$	DC operation See fig. 4	1.25					
Maximum thermal resistance, junction to case per package	sistance, package		DC operation	0.63	°C/W				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24					
Approximate weight				6	g				
Approximate weight				0.21	OZ.				
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf · cm (lbf · in)				
Mounting torque	maximum		Non-jublicated tilleads	12 (10)					
				40CP	Q035				
Marking device			Case style TO-247AC (JEDEC)	40CPQ040					
				40CP	Q045				

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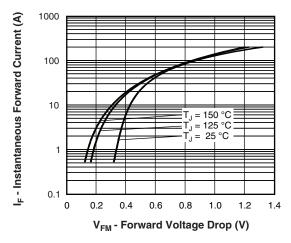


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

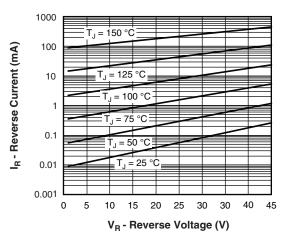


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

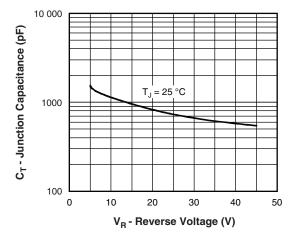


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

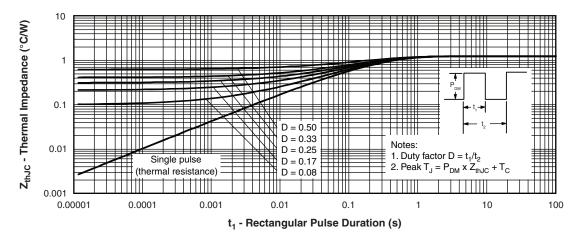


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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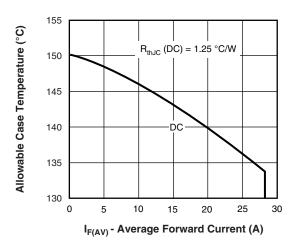


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

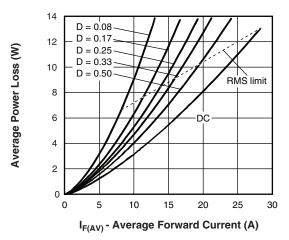


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

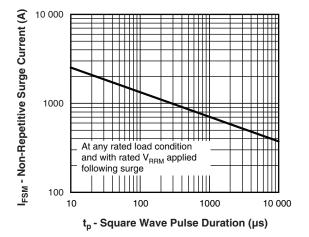


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

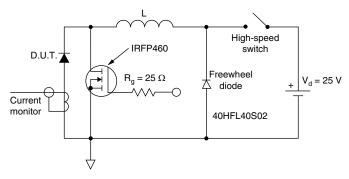


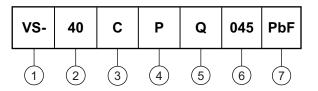
Fig. 8 - Unclamped Inductive Test Circuit

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#### **ORDERING INFORMATION TABLE**

Device code



Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

035 = 35 V 040 = 40 V

6 - Voltage code

040 - 40 V045 = 45 V

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-40CPQ035PbF	25	500	Antistatic plastic tube						
VS-40CPQ035-N3	25	500	Antistatic plastic tube						
VS-40CPQ040PbF	25	500	Antistatic plastic tube						
VS-40CPQ040-N3	25	500	Antistatic plastic tube						
VS-40CPQ045PbF	25	500	Antistatic plastic tube						
VS-40CPQ045-N3	25	500	Antistatic plastic tube						

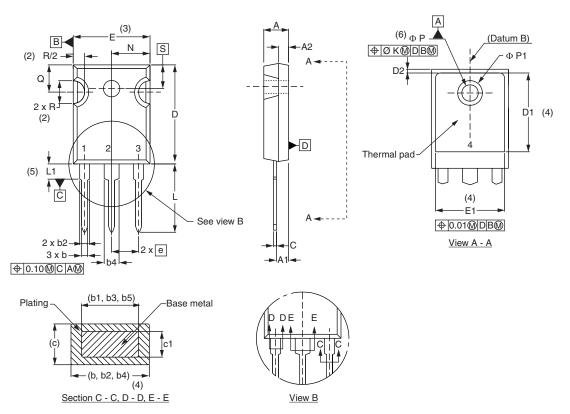
LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95223				
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226				
	TO-247AC -N3	www.vishay.com/doc?95007				



### Vishay Semiconductors

### **TO-247**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209		D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102		Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098		E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055		е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053		ØΚ	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133		ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035		Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4	S	5.51	BSC	0.217	BSC	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$  Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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### Vishay:

40CPQ035 40CPQ040 40CPQ045 VS-40CPQ035PBF VS-40CPQ040PBF VS-40CPQ045PBF