Vishay High Power Products

Schottky Rectifier, 2.1 A



- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\mathrm{C}$
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

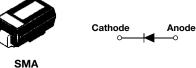
DESCRIPTION

The VS-10MQ040NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	DC	2.1	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	120	А		
V _F	1.5 Apk, T _J = 125 °C	0.56	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10MQ040NPbF	UNITS		
Maximum DC reverse voltage	V _R	40	M		
Maximum working peak reverse voltage	V _{RWM}	40	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 123 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1.5	А
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	120	A
non-repetitive surge current See fig. 6		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	30	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



PRODUCT SUMMARY				
I _{F(AV)}	2.1 A			
V _B	40 V			





VS-10MQ040NPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.54	v
		1.5 A		0.62	
		1 A	- T _J = 125 °C	0.49	
		1.5 A		0.56	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	mA
		T _J = 125 °C		26	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.36	V
Forward slope resistance	r _t			104	mΩ
Typical junction capacitance	CT	V_R = 10 V_{DC} , T_J = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	V1	F

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



DC

2.0

2.4

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D = 0.20 D = 0.25

D = 0.33

D = 0.75

See note (1)

0.4

Square wave (D = 0.50) 80 % rated V_R applied

0.8

1.2

1.6

150

140

130

120

110

100

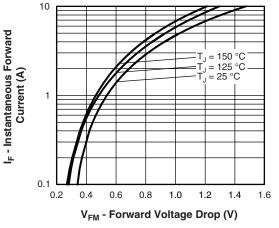
90

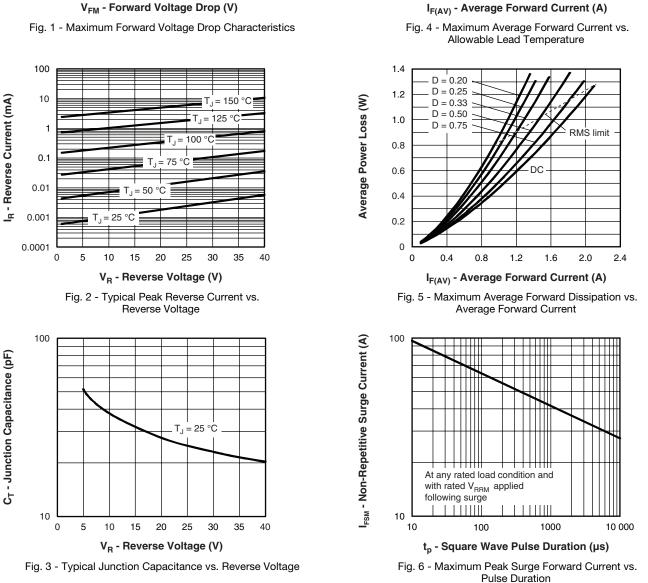
80

70

0

Allowable Case Temperature (°C)





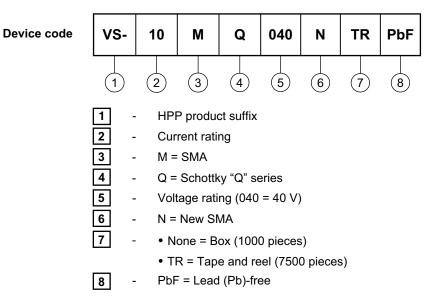
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95018		
Part marking information		www.vishay.com/doc?95029		
Deckeding information	Tape and reel	www.vishay.com/doc?95034		
Packaging information	Bulk	www.vishay.com/doc?95397		
SPICE model		www.vishay.com/doc?95277		

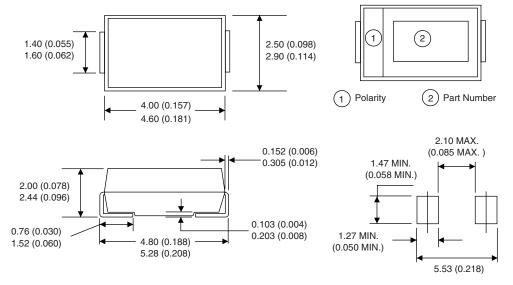


Outline Dimensions

Vishay High Power Products

SMA

DIMENSIONS in millimeters (inches)



Soldering pad



Vishay

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