

March 2013

FQP6N80C / FQPF6N80C N-Channel QFET MOSFET

800 V, 5.5 A, 2.5 Ω

Description

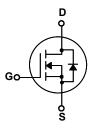
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 5.5 A, 800 V, $R_{DS(on)}$ = 2.5 Ω (Max) @V_{GS} = 10 V, I_D = 2.75 A
- Low Gate Charge (Typ. 21 nC)
- · Low Crss (Typ. 8 pF)
- 100% Avalanche Tested







Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		FQP6N80C	FQPF6N80C	Unit	
V _{DSS}	Drain-Source Voltage		800		V	
I _D	Drain Current - Continuous (T _C = 25°C)	1	5.5	5.5 *	Α	
	- Continuous (T _C = 100°C	C)	3.2	3.2 *	Α	
I _{DM}	Drain Current - Pulsed	(Note 1)	22	22 *	Α	
V _{GSS}	Gate-Source Voltage		± 30		V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	680		mJ	
I _{AR}	Avalanche Current	(Note 1)	5.5		Α	
E _{AR}	Repetitive Avalanche Energy (Note 1)		15.8		mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns	
P _D	Power Dissipation (T _C = 25°C)		158	51	W	
	- Derate above 25°C		1.27	0.41	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C	
TL	Maximum lead temperature for soldering purposes,		300		°C	
'L	1/8" from case for 5 seconds		300		C	

^{*} Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQP6N80C	FQPF6N80C	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.79	2.45	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	800			V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.97		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 800 V, V _{GS} = 0 V			10	μА
		V _{DS} = 640 V, T _C = 125°C			100	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Cha	aracteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 2.75 A		2.1	2.5	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 50 V, I _D = 2.75 A (Note 4)		5.4		S
Dynam C _{iss}	ic Characteristics Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		1010	1310	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		90	115	pF
C _{rss}	Reverse Transfer Capacitance			8	11	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V - 400 V I - 5 5 A		26	60	ns
t _r	Turn-On Rise Time	V_{DD} = 400 V, I_{D} = 5.5 A, R_{G} = 25 Ω		65	140	ns
t _{d(off)}	Turn-Off Delay Time	NG - 23 12		47	105	ns
t _f	Turn-Off Fall Time	(Note 4, 5)		44	90	ns
Qg	Total Gate Charge	V _{DS} = 640 V, I _D = 5.5 A,		21	30	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V		6		nC
Q _{gd}	Gate-Drain Charge	(Note 4, 5)		9		nC
	Source Diode Characteristics a	nd Maximum Ratings		I.	1	I
I _S	Maximum Continuous Drain-Source Diode Forward Current				5.5	Α
'5	Maximum Pulsed Drain-Source Diode Forward Current		l	-	-	l
	Maximum Pulsed Drain-Source Diode F	Forward Current			22	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F Drain-Source Diode Forward Voltage	Forward Current V _{GS} = 0 V, I _S = 5.5 A			22 1.4	A V

 dI_F / dt = 100 A/ μ s

(Note 4)

5.4

Q_{rr}

Notes:1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 42mH, I_{AS} = 5.5A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C 3. I_{SD} ≤ 5.5A, didtd \leq 200A μ_{B} , V_{DD} \leq BV $_{DS}$, Starting T_{J} = 25°C 4. Pulse Test : Pulse width \leq 300 μ_{B} , Duty cycle \leq 2% 5. Essentially independent of operating temperature

Reverse Recovery Charge

μС

Typical Characteristics

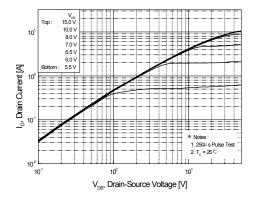


Figure 1. On-Region Characteristics

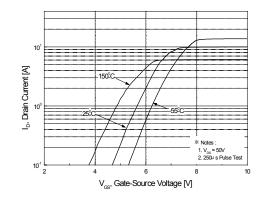


Figure 2. Transfer Characteristics

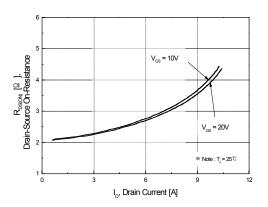


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

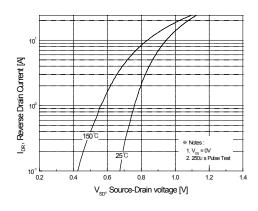


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

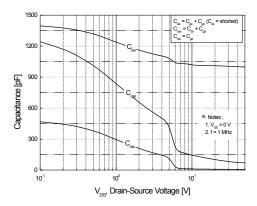


Figure 5. Capacitance Characteristics

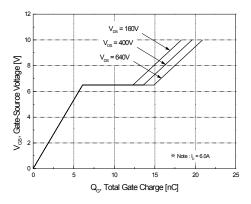


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

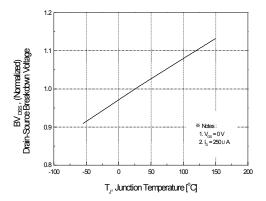


Figure 7. Breakdown Voltage Variation vs Temperature

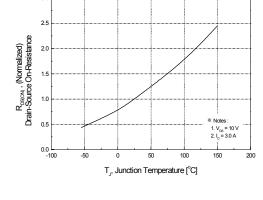


Figure 8. On-Resistance Variation vs Temperature

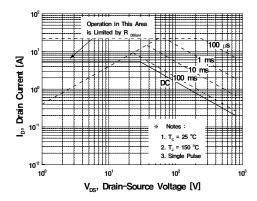


Figure 9-1. Maximum Safe Operating Area for FQP6N80C

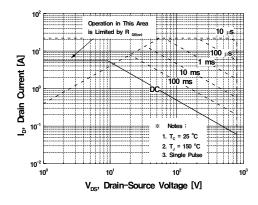


Figure 9-2. Maximum Safe Operating Area for FQPF6N80C

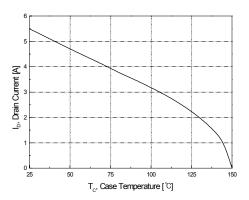


Figure 10. Maximum Drain Current vs Case Temperature

Typical Characteristics (Continued)

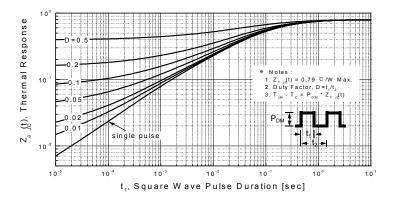


Figure 11-1. Transient Thermal Response Curve for FQP6N80C

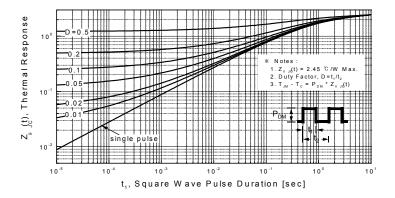
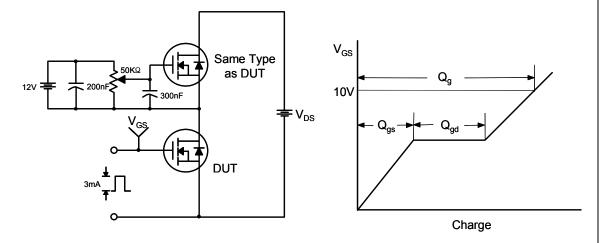
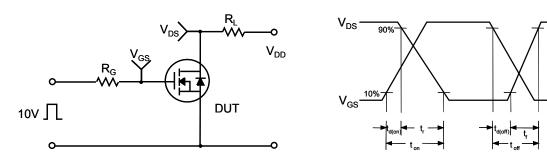


Figure 11-2. Transient Thermal Response Curve for FQPF6N80C

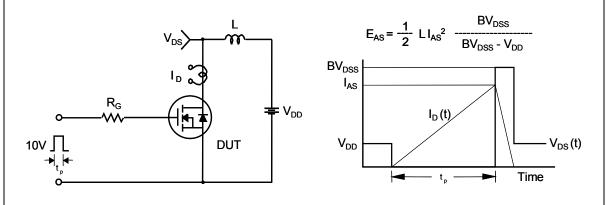
Gate Charge Test Circuit & Waveform



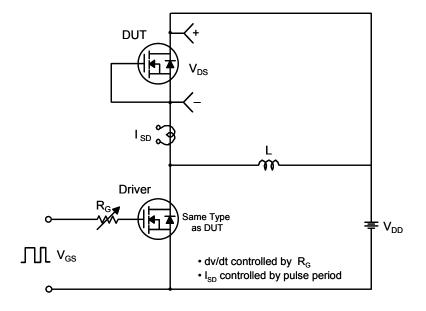
Resistive Switching Test Circuit & Waveforms

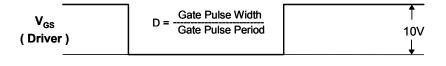


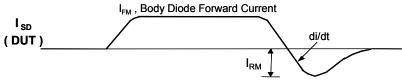
Unclamped Inductive Switching Test Circuit & Waveforms



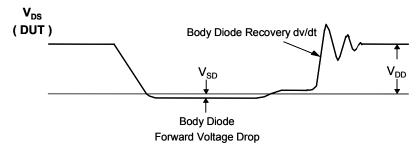
Peak Diode Recovery dv/dt Test Circuit & Waveforms

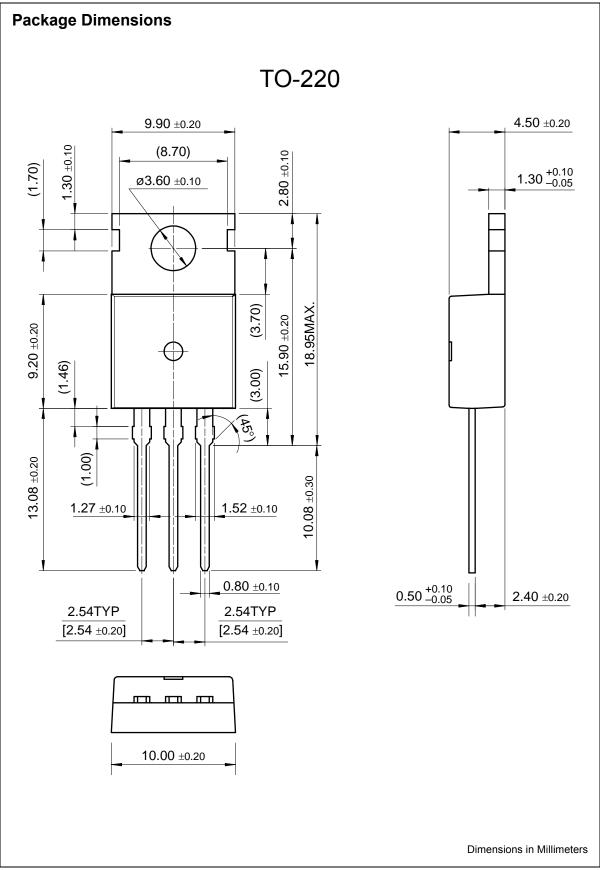


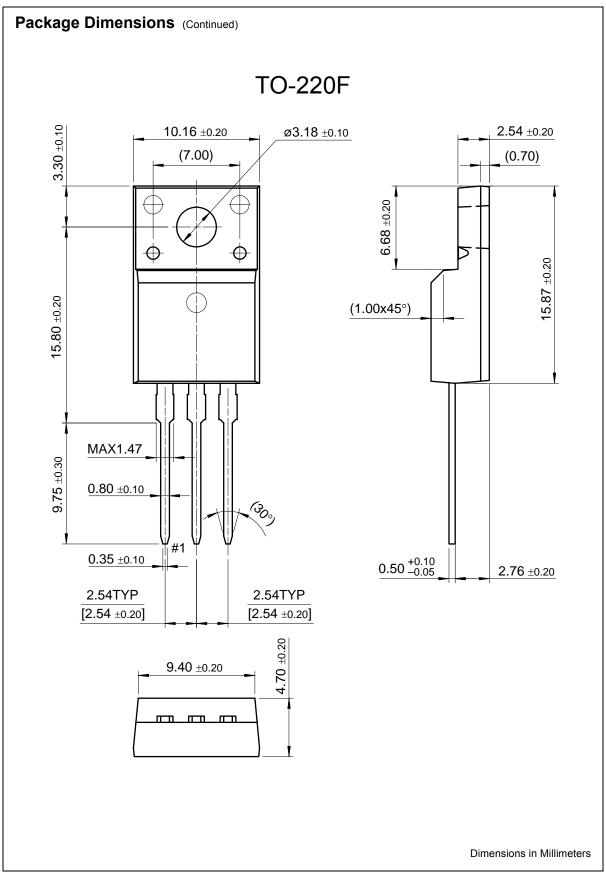




Body Diode Reverse Current











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